

**COLLEGE OF ENGINEERING ADOOR**

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

**TIME TABLE FOR S3 & S5 SUPPLEMENTARY LAB EXAMINATION JUNE 2018**

**CS13L1- Electronic Circuits Lab**

<b>Date</b>	<b>Time</b>
7/6/'18 ( June 7) <b>(THURSDAY)</b>	<b>10 AM – 1 PM</b>

**CS15L1 - Microprocessor lab**

<b>Date</b>	<b>Time</b>
8/6/'18( June 8) <b>(FRIDAY)</b>	<b>10 AM – 1 PM</b>

Students should submit certified fair records along with hall ticket at the time of examination.

**Adoor**

**Staff-in-Charge**

**HOD**

**25-05-2018**